

Serial No. 09/318,031Page 2

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IN THE SPECIFICATION:

Please replace the paragraph beginning on page 10, line 13 with the following rewritten paragraph:

Fig. 7 is a block diagram of a first embodiment of an SDARS receiver of the present invention. In the preferred embodiment, the full 12.5 MHz XM band containing the first and second ensembles are received in the receiver 200 via the antenna 110, a low noise amplifier 122 and an image filter 124 as per Fig. 5 & 6. The output of the image filter 124 is input to a first mixer 208. The first mixer 208 is driven by a VCO 221 which, in the illustrative embodiment, operates at a frequency of approximately 1600 MHz. The actual output frequency of the VCO 221 will be substantially equivalent to two-thirds of the center frequency of the full 12.5 MHz frequency band received at the antenna 110. If, for example, the center of the XM 12.5 MHz frequency band is 2338.750 MHz, the VCO should operate at two-thirds of 2338.750 MHz or 1559.167 MHz. The VCO is driven by a synthesizer 219.